## Notice of References Cited Application/Control No. 09/901,083 Examiner Paul B. Yanchus Applicant(s)/Patent Under Reexamination HIGASHIDA, MOTOKI Page 1 of 1

## **U.S. PATENT DOCUMENTS**

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*	_	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification	
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## **NON-PATENT DOCUMENTS**

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*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)					
	U	"IEEE Standard Test Access Port and Boundary-Scan Architecture", 1993, Institute of Electrical and Electronics Engineers, Inc., pages 9-14					
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

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